

FIG 1

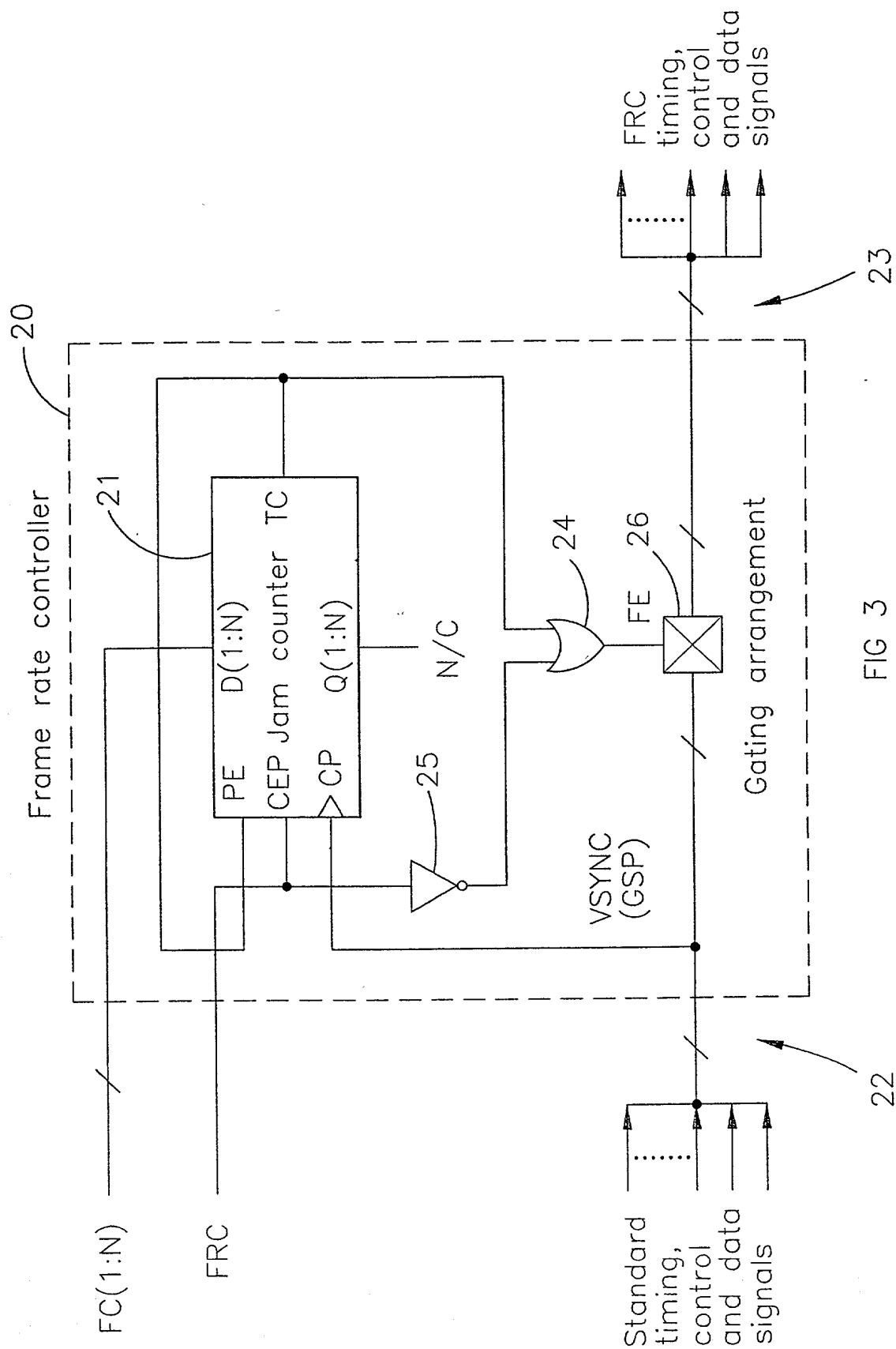


FIG 3

FRC=high;FC(1:4)=1101(=13)

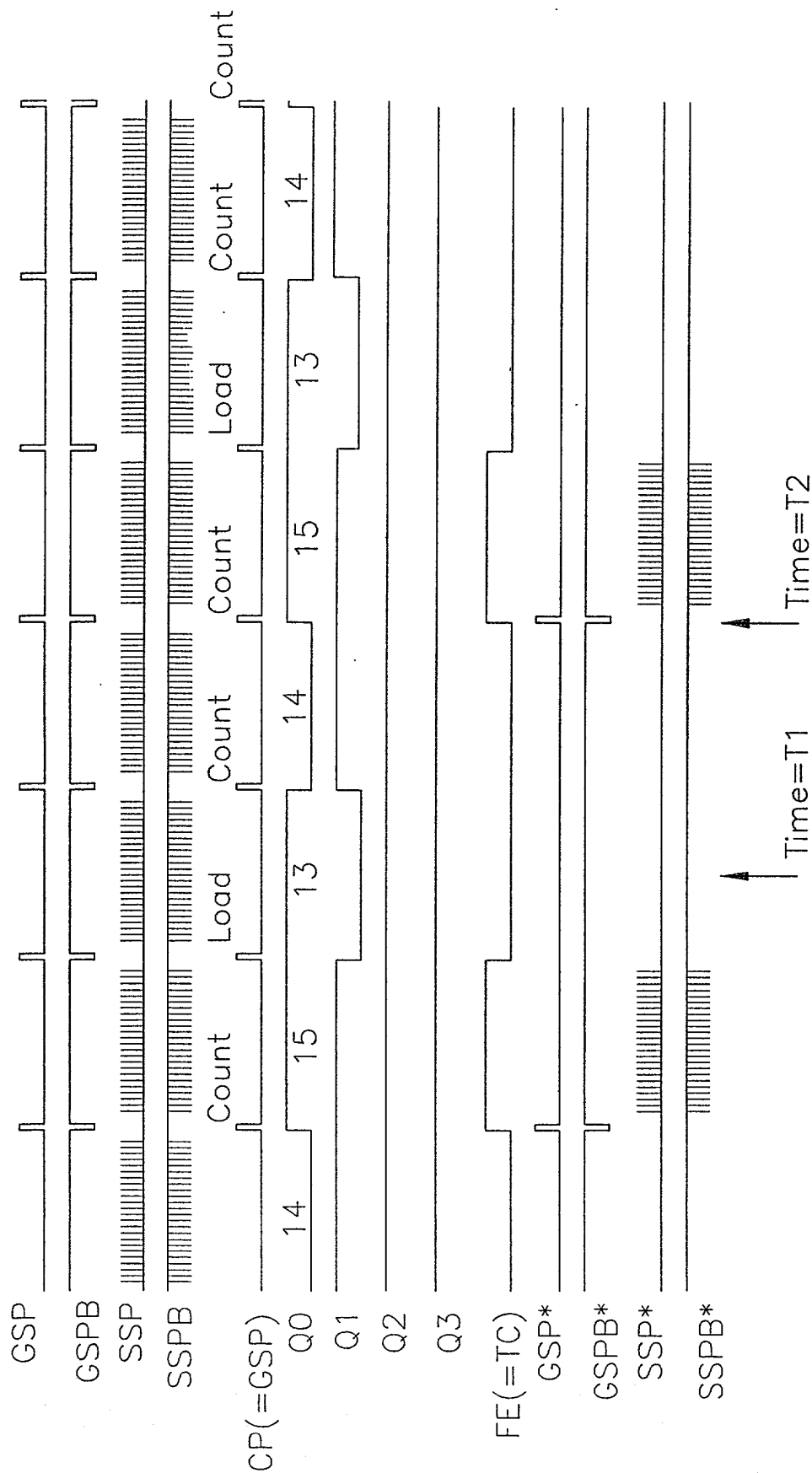
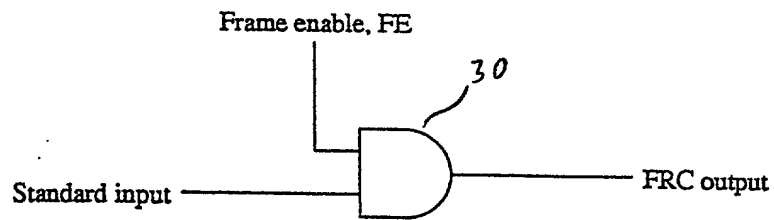
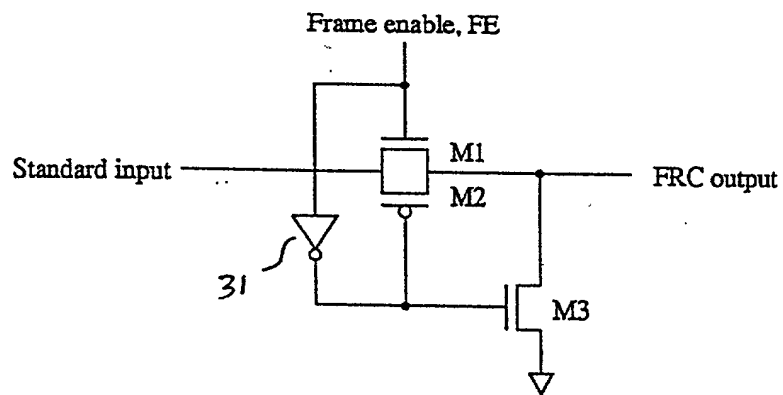


FIG 4



(a)



(b)

Figure 5:

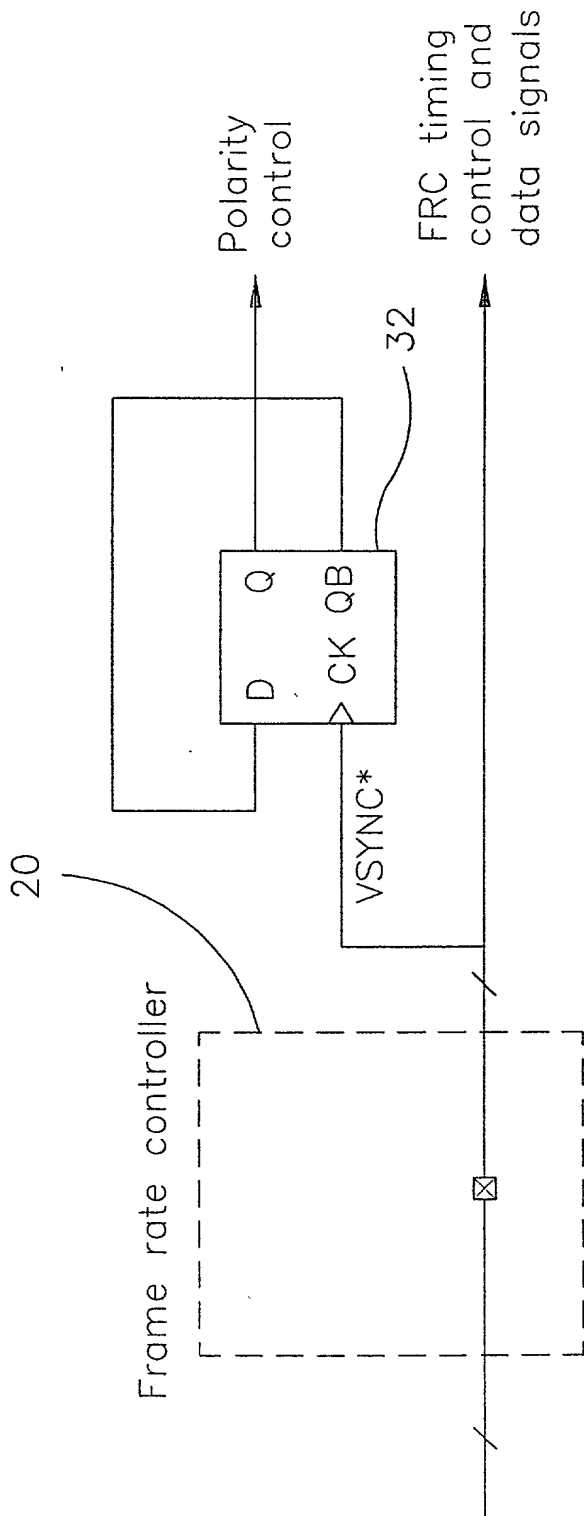
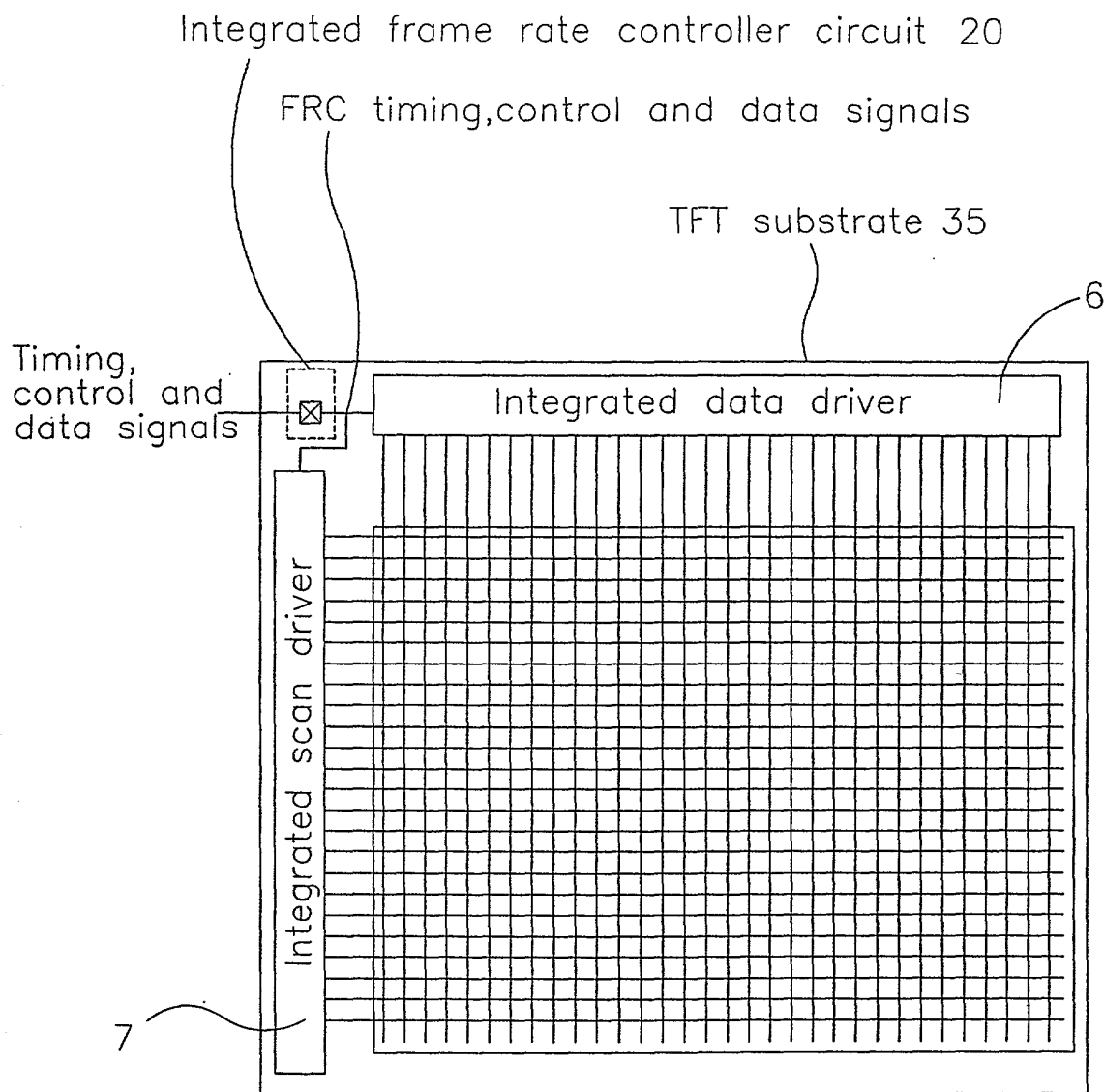
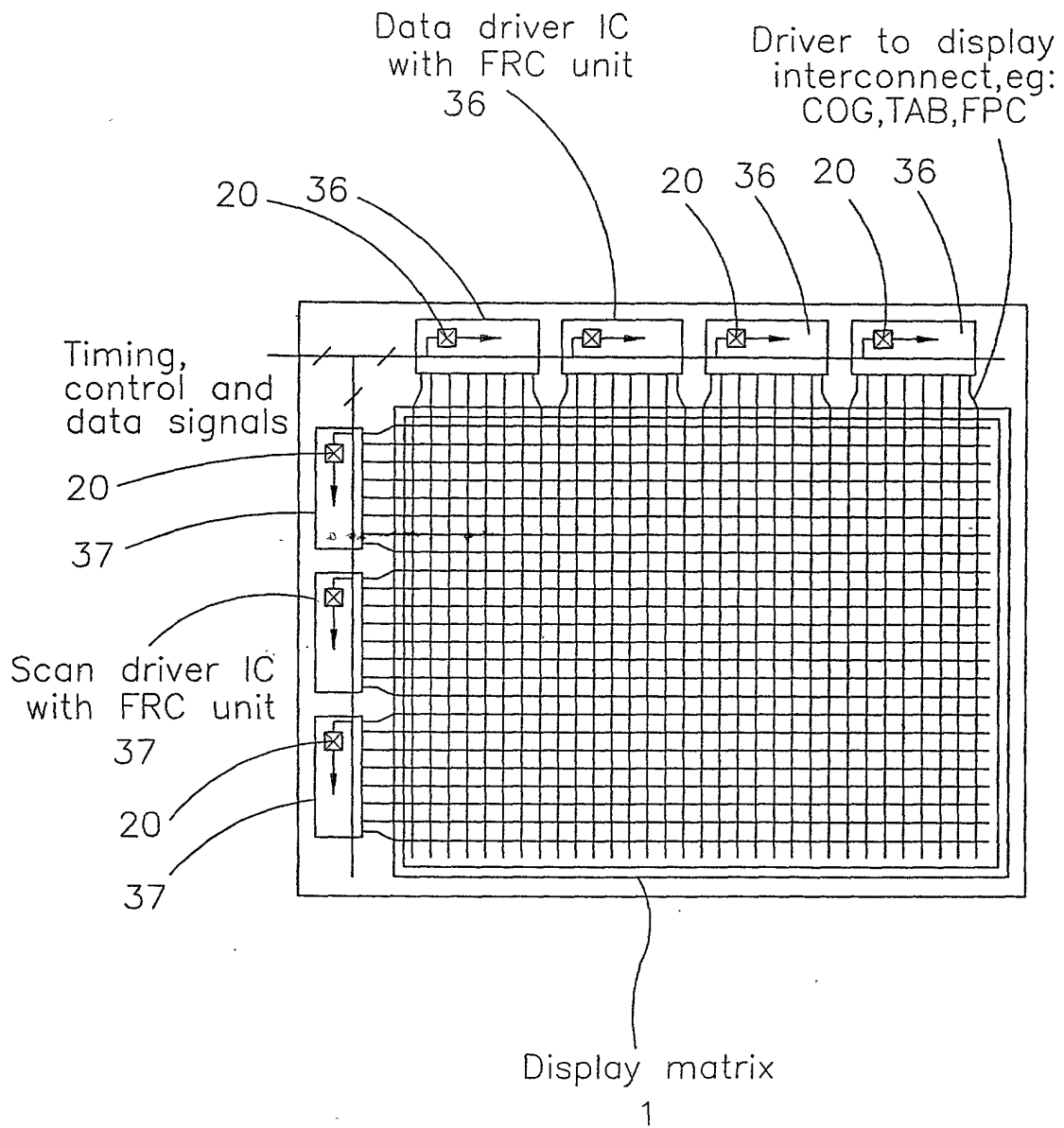


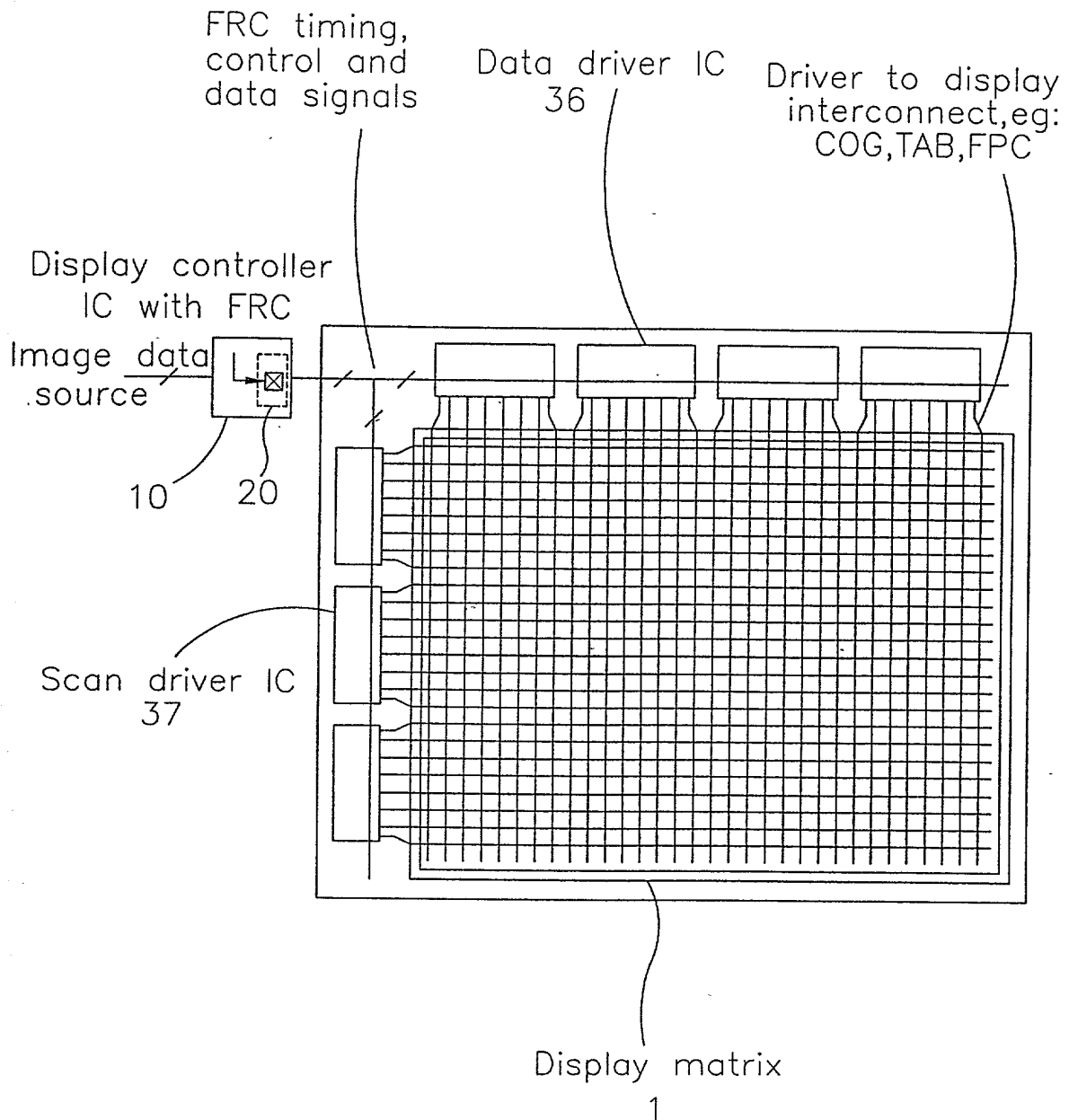
FIG 6



Active matrix display with integrated drivers
and frame rate controller

FIG 7

FIG 8

FIG 9

For continuation see FIG 10b

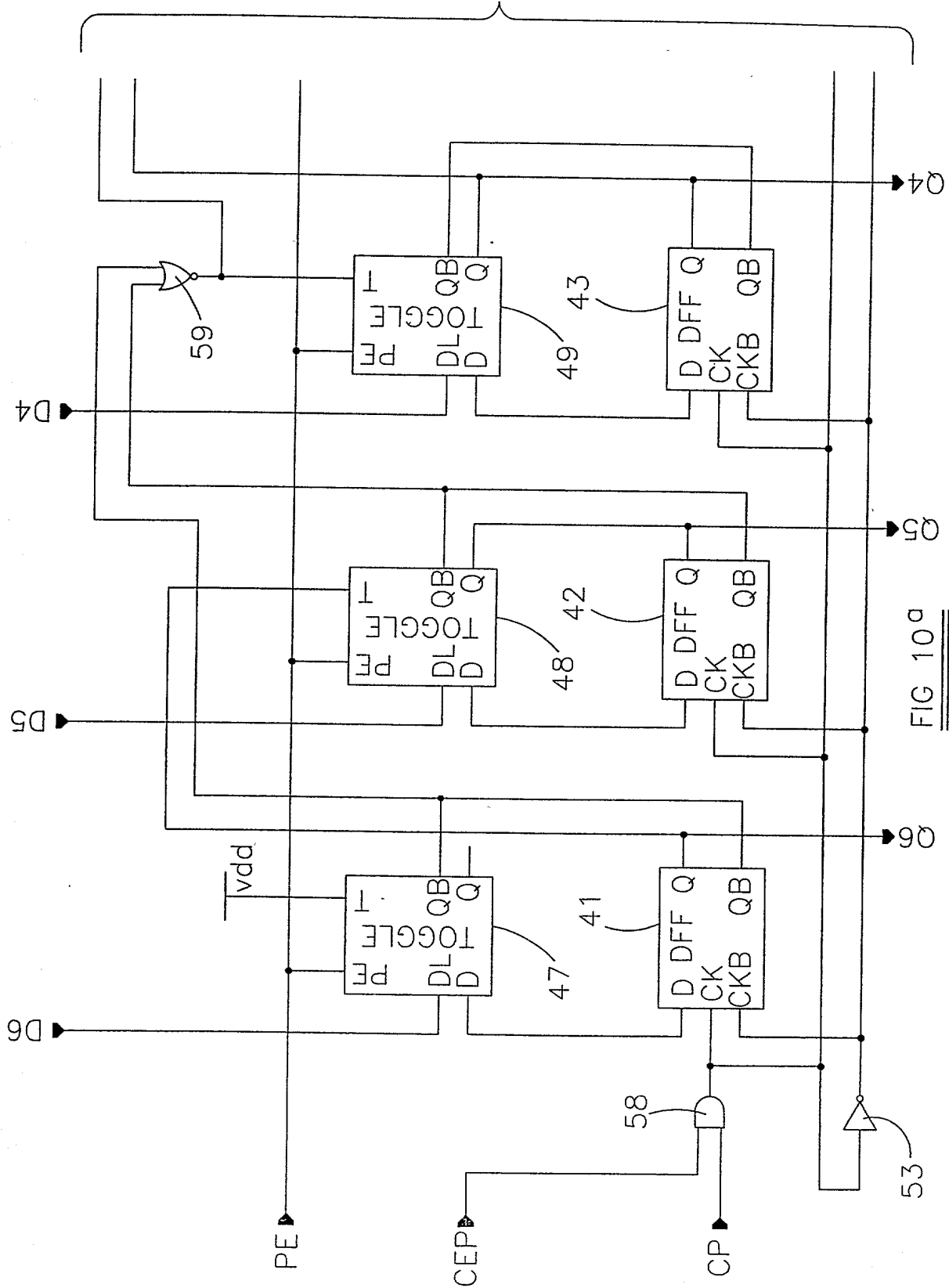


FIG 10a

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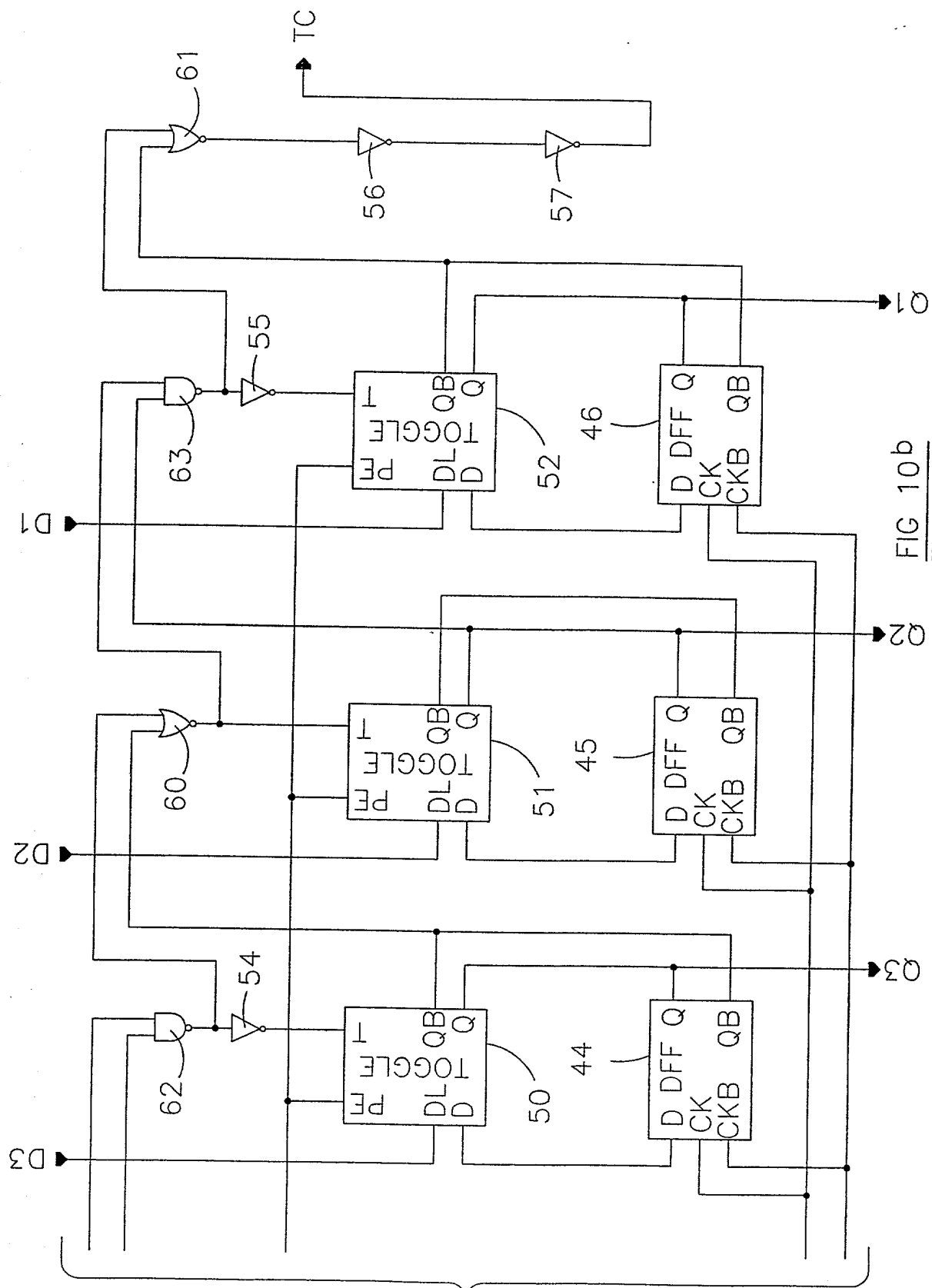


FIG 10b

For continuation see FIG 10a

[illegible]

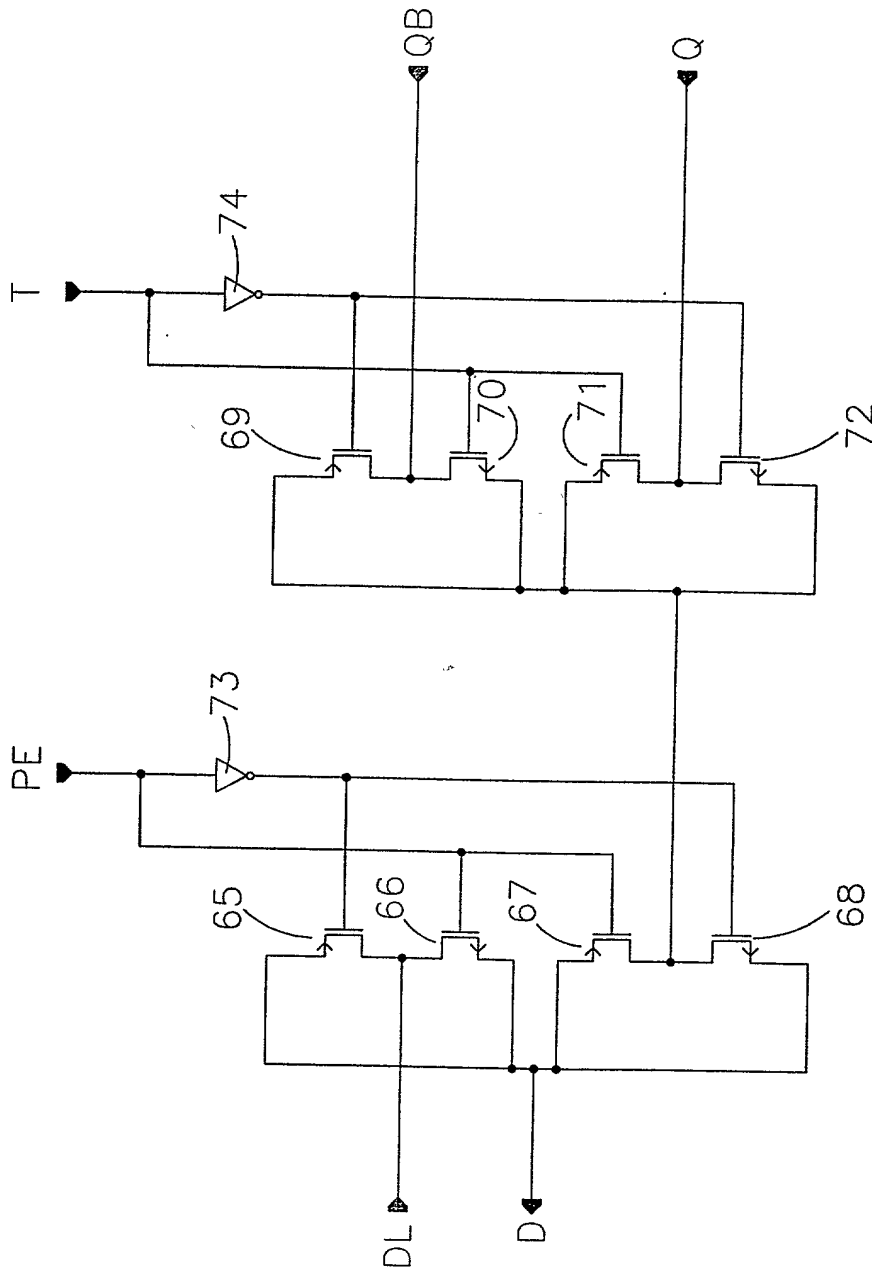


FIG 11

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